

LP-ARM9-2410-SYSTEM

ARM Chip System Design & Experiment Platform /Bring You into System & Embedded World/

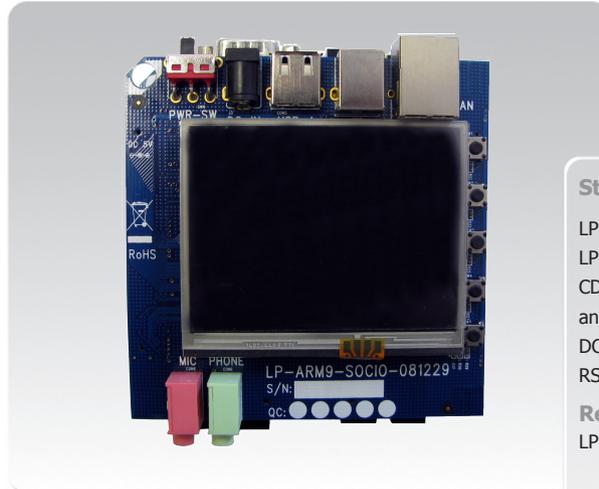
Introduction

Samsung S3C2410 is maturest in the ARM9 chip family, e.g. technical documents, referring books, supported systems, peripheral devices..... and so rich and diversification. It can be said that it the best choice for learning ARM SYTEM. The LP-ARM9-2410-SYTEM provides simple design which allow you to quickly access SOC world, as well as laid good basis of the ability and practical experience for the embedded design.

Moreover, the LP-ARM9-2410-SYTEM compact structure with 3.5 inch touchscreen LCD, 10/100M Ethernet, USB, 250,000 logic gate FPGA and integrity of peripheral interface allow you to easily understand SYTEM and Embedded. And you can use this platform for project development and products systematic.

Features

- Support Samsung S3C24C10 x 32-bits RISC Microprocessor which is under SARM920T frame.
- Use Xilinx Spartan-3E FPGA XC3S250E PQFP208 which contains 250,000 logic gate counts. Let you process ARM+FPGA experiments or topic research.
- Modular design, able to change other CUP boards for learning.
- Provide BootLoader function, able to use C or Assembly for writing Language, via the cable to download to the LP-2900 platform for experiment and verification.
- Able to connect to PC via RS-232 interface. Use the simple terminal program for control and process program download and execute.
- Provide JTAG interface, when writing the program, able to use ADS or RealView for single step trace and debug.
- Provide logic analyzer interface, able to view the SPI, I2C, UART and PWM waveform for analysis. If you use Leaptronix LA or PLA, you can view the protocol decoder as well.
- If you already have LP-2900, you just purchase LP-ARM9-2410 module for upgrade to ARM9 training kit.



- ### Standard Accessories
- LP-ARM9-SOCIO.....x1
 - LP-ARM9-2410-kit.....x1
 - CD, included demo program and manual.....x1
 - DC adaptor 5V/1.6A.....x1
 - RS-232 to USB cable.....x1
- ### Related Products
- LP-2900

Specifications

CPU	SAMSUNG S3C24C10x
Memory	SDRAM 64MBytes
	NOR FLASH 4MBytes
	NAND FLASH 64MBytes
Display	3.5 inch TFT LCD, 320 X 240 pixels
Touch interface	4 line resisance type
Net interface	10/100M Ethernet
Communication interface	RS-232
Sound interface	IIS sound interface chip, Stereo audio output interface
	Microphone input interface
JTAG interface	ARM standard JTAG interface
FPGA	Xilinx XCS250E PQFP208
Button	5 button key
Power	100V~240V AC, auto switching
	50/60 Hz
Dimension	32 x 22.6 x 3.0 cm
Weight	500g
Operation altitude	5000m
Operation humidity	20%~70% non-condensing

Recommended Bundling Instruments

- Leaptronix LA-2025 / LA-2050 / PLA-1016 / PLA-2532